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U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE
STATEMENT

Docket Number:
2885/21

Application Number
09/335,974

Filing Date
June 18, 1999

Examiner
RAY, G.

Art Unit
2181

Invention Title
I/O AND MEMORY BUS SYSTEM FOR DFPS AND
UNIT WITH TWO OR MULTI-DIMENSIONAL
PROGRAMMABLE CELL ARCHITECTURE

Inventor(s)
Martin VORBACH et al.

I hereby certify that this correspondence is being deposited with the
United States Postal Service as first class mail in an envelope addressed
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Assistant Commissioner for Patents
Washington D.C. 20231
Box Patent Application

Date: Jan 18, 2001 Reg. No. P47893
Signature:
Andrew L. Reibman

- I. In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorneys for Applicants hereby bring the references listed on the attached modified PTO Form No. 1449 to the attention of the Examiner. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.
- II. A copy of each patent, publication or other information listed on the modified PTO form 1449 is enclosed, except as otherwise indicated.
- III. It is believed that no fees are due in connection with this Information Disclosure Statement. However, should any fees be due, the Commissioner is authorized to charge Deposit Account Number 11-0600 for such fees. A duplicate copy of this communication is enclosed for charging purposes.

Dated:

By:
Andrew L. Reibman (Reg. No. P47893)

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KENYON & KENYON
One Broadway
New York, N.Y. 10004
(212) 425-7200 (telephone)
(212) 425-5288 (facsimile)